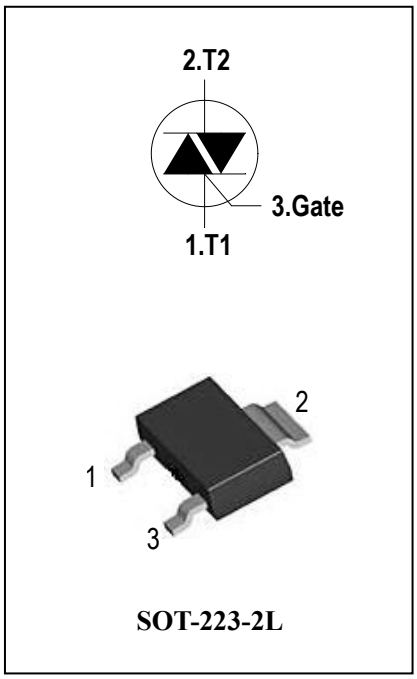
3Quadrants Triacs

General Description

This device is suitable for low power AC switching application, phase control application such as fan speed and temperature modulation control , lighting control and static switching relay also designed for use in MPU interface, TTLlogic.



Features

- Repetitive Peak Off-State Voltage: 600Vand800V
- R.M.S On-State Current ($I_{T(RMS)}$= 2 A)
- High Commutation dv/dt⩾500V/uS
- These Devices are Pb-Free and are RoHS Compliant

Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM}	Repetitive Peak Off-State Voltage	Tj = 25°C	ADT2C60WL	600	V
V_{RRM}			ADT2C80WL	800	V
$I_{T(RMS)}$	R.M.S On-State Current	T_C = 75 °C		2	A
I_{TSM}	Surge On-State Current	tp=20ms(50Hz)/tp=16.7ms(60Hz)		16/17	A
I^2t	I^2t for fusing	tp=10ms		1.28	A^2s
dI/dt	Critical rate of rise of on-state current	F = 120 Hz Tj = 125°C I_G = 2 x I_{GT} , tr ≤ 100 ns		50	A/μs
I_{GM}	Peak Gate Current	tp = 20 μs Tj = 125°C		1	A
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=125°C)			0.2	W
P_{GM}	Peak Gate Power Dissipation(tp=20us,Tj=125°C)			1	W
T_j	Operating Junction Temperature			- 40 ~ 125	°C
T_{STG}	Storage Temperature			- 40 ~ 150	°C

ADV
Scan the QR code to view the latest product information

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Symbol	Items		Conditions		ADT2C60WL/80WL	Unit
I_{DRM}	Peak Forward Reverse Blocking Current		V_{DRM} = V_{RRM}, Tj = 25°C	Max.	10	uA
I_{RRM}			V_{DRM} = V_{RRM}, Tj = 125°C		0.5	mA
V_{TM}	Peak On-State Voltage		I_{TM} = 2A, t_p = 380 μs	Max.	1.6	V
V_{GD}	Q1-Q2-Q3	Non−Trigger Gate Voltage	V_D = V_{DRM} R_L = 3.3 kΩ Tj = 125°C	Min.	0.2	V
V_{GT}	Q1-Q2-Q3	Gate Trigger Voltage	V_D = 12V ， R_L = 33Ω	Max.	1.5	V
I_{GT}	Q1-Q2-Q3	Gate Trigger Current		Max.	10	mA
I_H	Q1-Q2-Q3	Holding Current	I_T = 0.1A	Max.	10	mA
I_L	Q1-Q3	Latching Current	I_G = 1.2 I_{GT}	Max.	15	mA
	Q2				25	
dV/dt	Critical Rate of Rise of Off-State Voltage		V_D = 2/3V_{DRM} gate open Tj = 125°C	Min.	500	V/μs
(dV/dt)c	Rate of Change of Commutating Current,		(dI/dt)c=-0.5A/ms Tj = 125°C	Min.	10	V/μs
$R_{th(j-c)}$	Junction to case (AC)			Max.	25	°C/W
$R_{th(j-a)}$	Junction to ambient(Copper surface under tab:S=5cm^2)			Max.	60	°C/W

FIG.1:Triac quadrant are defined and the gate trigger test circuit

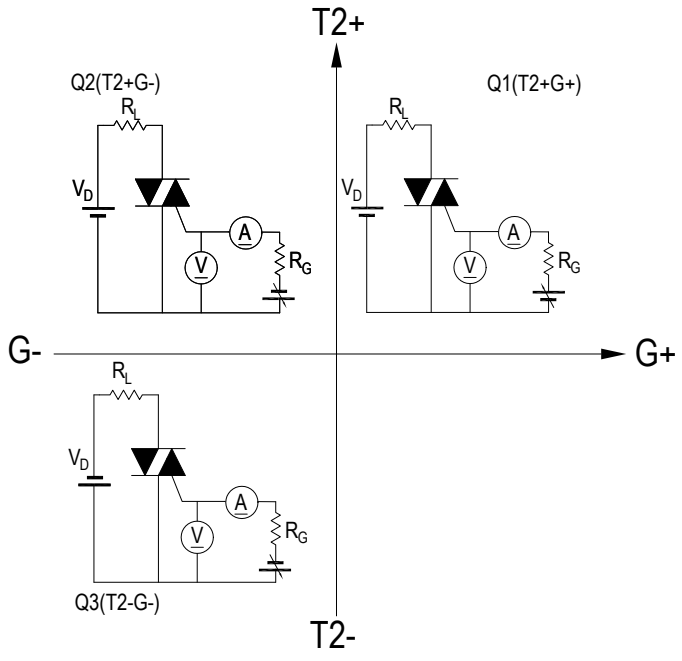


FIG.2: Maximum on-state power dissipation

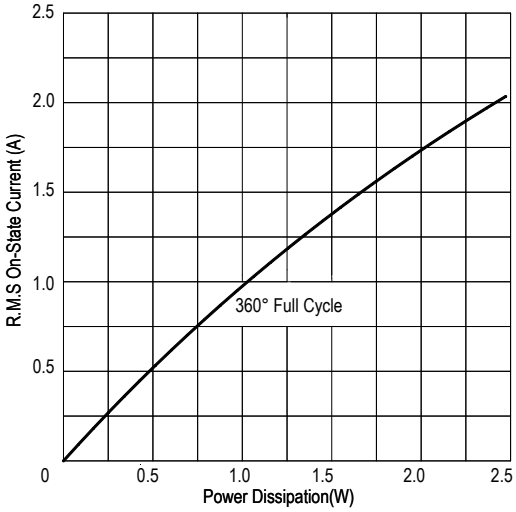


FIG.3: Typical RMS on-state current VS Allowable case Temperature

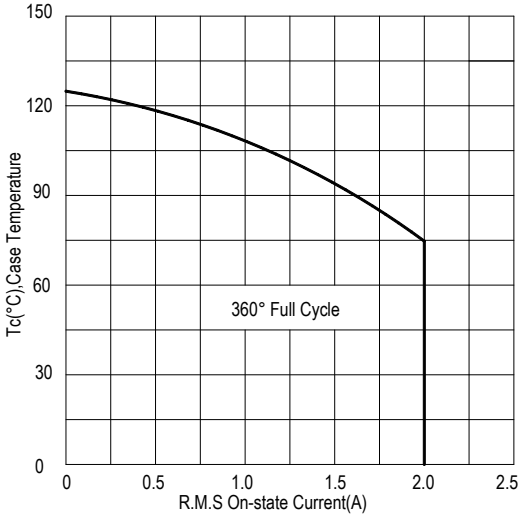


FIG.4: Gate trigger current VS Junction temperature

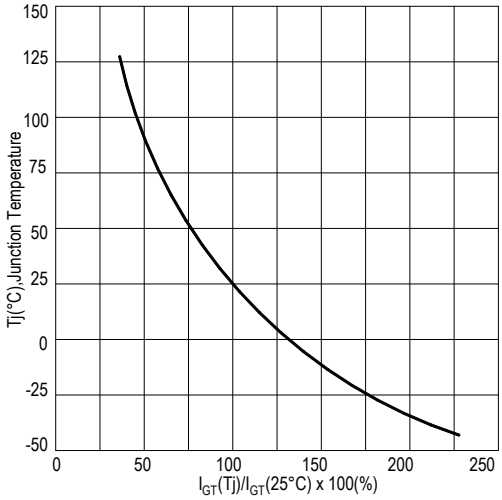


FIG.5: Rated surge on-state current (Non-Repetitive)

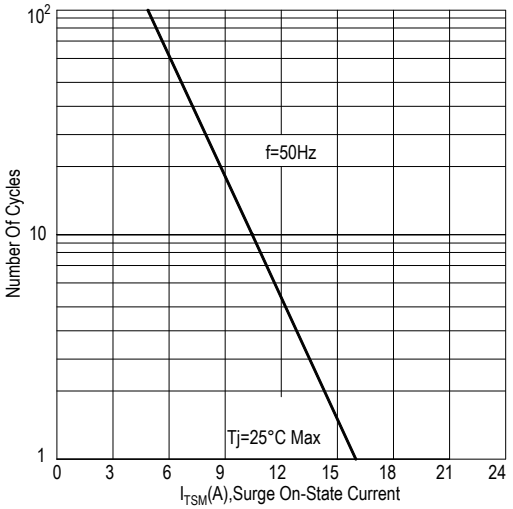


FIG.6: On-state characteristics(Max)

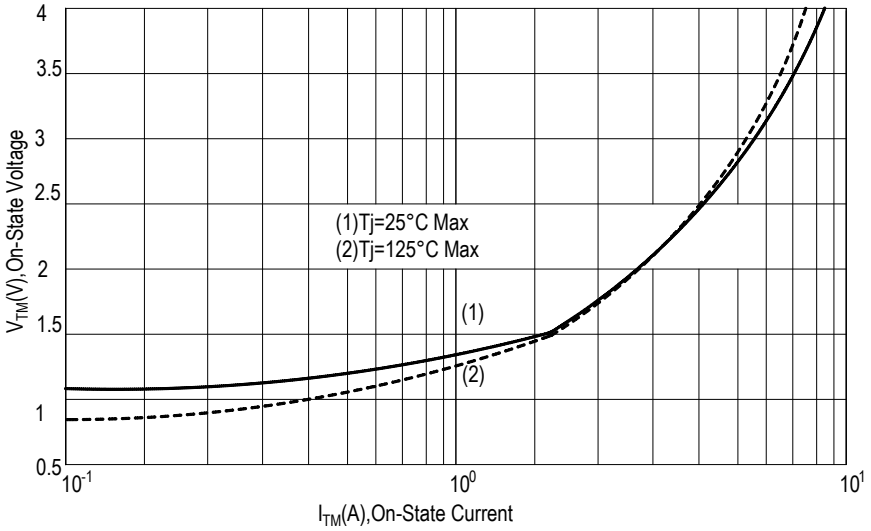


FIG.7:Holding current and Latching current VS Junction temperature

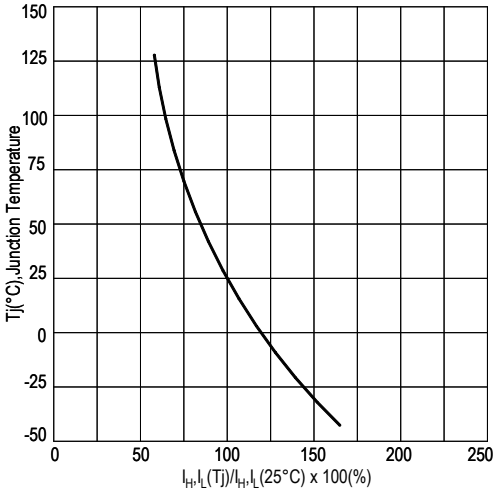
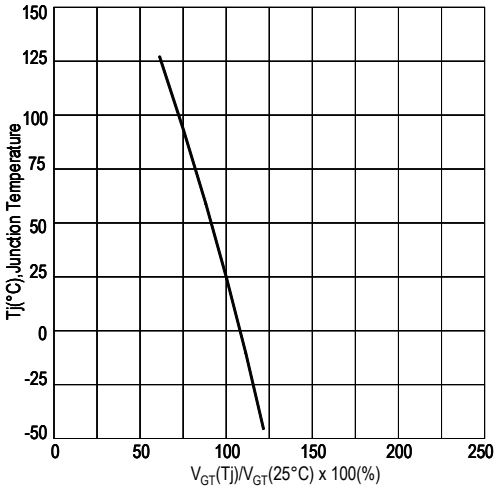
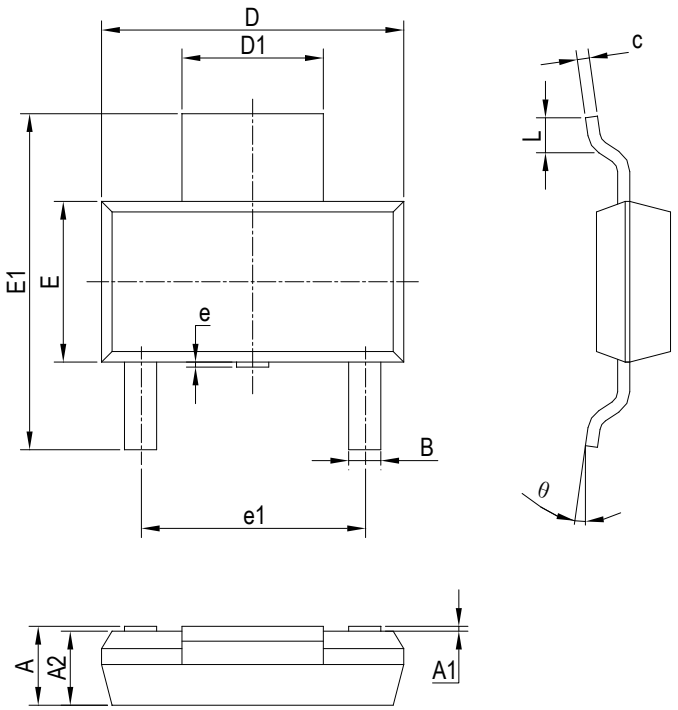


FIG.8: Gate trigger voltage VS Junction temperature



PACKAGE MECHANICAL DATA

SOT-223-2L Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.500	1.800	0.059	0.071
A1	0.010	0.100	0.001	0.004
A2	1.500	1.700	0.059	0.067
c	0.220	0.350	0.009	0.014
D	6.200	6.700	0.244	0.264
D1	2.900	3.100	0.114	0.122
E	3.300	3.700	0.130	0.146
E1	6.700	7.300	0.264	0.287
e	0	0.200	0	0.008
e1	4.500	4.700	0.177	0.185
L	0.700	1.150	0.028	0.045
θ	0°	10°	0°	10°
B	0.600	0.800	0.024	0.031

Making Diagram

ADV:Logo
ADT2C60WL:Part number
X:Internal control code
H:Halogen Free

AD T 2 C 60 WL

ADVANCED
Internal control code
Current:2=2A
Quadrant:C=3Q
Voltage:60=600V 80=800V
Package explain:WL=SOT-223-2L

Ordering information

Part number	Package	Marking	Packing	Quantity
ADT2C60WL	SOT-223	ADT2C60WL	Embossed tape	4000pcs
ADT2C80WL	SOT-223	ADT2C80WL	Embossed tape	4000pcs

Notice

1. All information included in this document such as product data, diagrams, charts, programs, algorithms, and application circuit examples, is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any ADV products listed in this document, please confirm the latest product information with a ADV sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by ADV such as that disclosed through our website. (http://www.advsemi.com)
2. ADV has used reasonable care in compiling the information included in this document, but ADV assumes no liability whatsoever for any damages incurred as a result of errors or omissions in the information included in this document.
3. You should use the products described herein within the range specified by ADV, especially with respectvto the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. ADV shall have no liability for malfunctions or damages arising out of the use of ADV products beyond such specified ranges.
4. When using or otherwise relying on the information in this document, you should evaluate the information in light of the total system before deciding about the applicability of such information to the intended application.ADV makes no representations, warranties or guaranties regarding the suitability of its products for any particular application and specifically disclaims any liability arising out of the application and use of the information in this document or ADV products.
5. Although ADV endeavors to improve the quality and reliability of its products, IC products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a ADV product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other applicable measures. Among others, since the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
6. This document is provided for reference purposes only so that ADV customers may select the appropriate ADV products for their use. ADV neither makes warranties or representations with respect to the accuracy or completeness of the information contained in this document nor grants any license to any intellectual property rights or any other rights of ADV or any third party with respect to the information in this document.
7. ADV shall have no liability for damages or infringement of any intellectual property or other rights arising out of the use of any information in this document, including, but not limited to, product data, diagrams, charts,programs, algorithms, and application circuit examples.
8. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written approval from ADV.